

L Number	Hits	Search Text	DB	Time stamp
1	4680	memory same (inverters or switch\$5 or TFT or \$MOS) same pixel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 14:54
2	3192	(memory same (inverters or switch\$5 or TFT or \$MOS) same pixel) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 14:55
3	677	((memory same (inverters or switch\$5 or TFT or \$MOS) same pixel) and @ad<20010118) and 345/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 14:56
4	192	((((memory same (inverters or switch\$5 or TFT or \$MOS) same pixel) and @ad<20010118) and 345/\$.ccls.) and optical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 14:56
-	28025	pixel with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 14:15
-	7895	pixel near2 memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 14:16
-	64	(pixel near2 memory) same (inverter or MOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 14:51
-	78	(pixel adj2 memory) same (inverter\$1 or \$MOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 14:52
-	40	((pixel adj2 memory) same (inverter\$1 or \$MOS)) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 14:59
-	3403	pixel same memory same (inverter\$1 or \$MOS or switch\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:00
-	2503	(pixel same memory same (inverter\$1 or \$MOS or switch\$2)) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:01
-	1100	((pixel same memory same (inverter\$1 or \$MOS or switch\$2)) and @ad<20010118) and optical\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:02

-	49	((pixel same memory same (inverter\$1 or \$MOS or switch\$2)) and @ad<20010118) and optical\$5) and (OLED or EL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:10
-	2596	(pixel with memory) same (inverter\$1 or switch\$4 or \$MOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:13
-	2596	((pixel with memory) same (inverter\$1 or switch\$4 or \$MOS)) and 2ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:14
-	1887	((pixel with memory) same (inverter\$1 or switch\$4 or \$MOS)) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:15
-	62	((pixel with memory) same (inverter\$1 or switch\$4 or \$MOS)) and @ad<20010118) and (OLED or EL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 15:25
-	45095	pixel same (memory or \$mos)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:47
-	2223823	inverter\$1 or switch\$2 or TFT\$1 or (P near2 N (TFT\$1 or switch\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:03
-	5489	(pixel same (memory or \$mos)) same (inverter\$1 or switch\$2 or TFT\$1 or (P near2 N (TFT\$1 or switch\$2)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:49
-	36	((pixel same (memory or \$mos)) same (inverter\$1 or switch\$2 or TFT\$1 or (P near2 N (TFT\$1 or switch\$2)))) same OLED	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:03
-	2170355	inverter\$1 or switch\$2 or TFT\$1 or (P adj type or N adj type near2 (TFT\$1 or switch\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:04
-	5549	(pixel same (memory or \$mos)) same (inverter\$1 or switch\$2 or TFT\$1 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:05
-	212	((pixel same (memory or \$mos)) same (inverter\$1 or switch\$2 or TFT\$1 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)))) and OLED	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:05
-	30	((pixel same (memory or \$mos)) same (inverter\$1 or switch\$2 or TFT\$1 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)))) and OLED) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:24

-	2040105	inverter or switch\$2 or TFT\$2 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)) same OLED	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:26
-	5284	(pixel same (memory or \$mos)) same (inverter or switch\$2 or TFT\$2 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)) same OLED)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:27
-	3453	((pixel same (memory or \$mos)) same (inverter or switch\$2 or TFT\$2 or (P adj type or N adj type near2 (TFT\$1 or switch\$2)) same OLED)) and @ad<20010118	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:28
-	58732	\$MOS same (switch\$2 or inverter\$1 or TFT or ((P or N)) near (TFT or switch\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:30
-	2150	(\$MOS same (switch\$2 or inverter\$1 or TFT or ((P or N)) near (TFT or switch\$2))) same pixel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:31
-	27	(((\$MOS same (switch\$2 or inverter\$1 or TFT or ((P or N)) near (TFT or switch\$2))) same pixel) same OLED	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:36